Block V Solar Cell Module
Design and Test Specification for
Intermediate Load Applications – 1981

LSA Engineering Area

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Jet Propulsion Laboratory
California Institute of Technology
Pasadena, California
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SECTION I
INTRODUCTION

A. SCOPE

This document establishes the requirements for the design and test of terrestrial solar cell modules for one phase of DOE's Low-Cost Solar Array Project. Intermediate-load modules designed to meet this specification will generally have the following design features and characteristics:

(1) Nominal Operating Voltage ($V_{no}$) between 5 Vdc and 20 Vdc.

(2) Ability to be series-connected to worst-case open-circuit voltages of 1000 Vdc.

(3) Dimensions not exceeding 1.22 m x 2.44 m (4 ft x 8 ft).

(4) Flat-plate configuration (non-concentrating).

(5) Output power referenced to Nominal Operating Conditions (NOC) and $V_{no}$.

In addition to module design and performance requirements, a series of characterization and qualification tests are also specified.

B. APPLICABLE DOCUMENTS

The following documentation is applicable to the extent specified herein. In the event of conflict between this specification and these documents, this specification shall apply.


(2) Jet Propulsion Laboratory documents:

(a) Drawing No. 10082854, Latest Revision, Interface Control Drawing for LSA ILC Module.


SECTION II
DESIGN AND PERFORMANCE REQUIREMENTS

Solar cell modules meeting the requirements of this document will be mounted or grouped into array structures compatible with system design constraints for intermediate-load-center (ILC) applications ranging from 10 kW to 500 kW. Such applications are typified by apartment complexes, water-pumping installations, shopping centers, and small industrial facilities.

The module design shall satisfy general design requirements including consideration of features that will minimize possible degradation of module performance and physical properties resulting from solar exposure, environmental weathering, soiling, mechanical loading, and array shadowing. In addition to these general considerations, the specific performance and design requirements set forth below shall be met by the modules.

The use of concentrators shall not be considered in meeting these requirements.

A. PERFORMANCE REQUIREMENTS

The following standard performance measurement and reporting requirements shall be used:

1. Nominal Operating Voltage

The Nominal Operating Voltage \( V_{no} \) is the reference voltage at which modules are designed to provide maximum output power at NOC. NOC is defined as an irradiance level of 80 mW/cm², Air Mass 1.5 spectrum, and cell temperature equal to the Nominal Operating Cell Temperature (NOCT). For purposes of integration in ILC applications, \( V_{no} \) shall be not less than 5 Vdc. For purposes of standardization, it is recommended that \( V_{no} \) be 15.0 Vdc; however, a convenient fraction of 15 Vdc, such as 5, 7.5, 10, or 20 Vdc, may be selected. For safety reasons, \( V_{no} \) shall in all cases be selected such that the maximum open-circuit voltage \( V_{oc} \) shall not exceed 30 Vdc under worst-case conditions of 100 mW/cm² and 0°C, cell temperature.

2. Module Output Power

Module Output Power (\( P \)) for individual modules shall be determined by measurements referenced to NOC and \( V_{no} \), in accordance with the procedures described in Section IV, Part A. For purposes of accepting production modules, the Average Module Output Power (\( P_{avg} \)) shall be established by measurement of \( P \) for a suitably-sized sample quantity of modules (not fewer than 10).
3. Minimum Individual Module Output Power

The minimum acceptable output power ($P_{\text{min}}$) for individual production modules shall be not less than 90% of the pre-determined $P_{\text{avg}}$.

4. Module Peak Power Rating

The peak power rating ($P_p$) of the module shall be stated at 100 mW/cm$^2$ irradiance, AM 1.5 spectrum, and 25°C cell temperature on the basis of measurement of the average maximum power of the same sample used to establish $P_{\text{avg}}$.

5. Nominal Operating Cell Temperature

NOCT is the average cell junction temperature in the field under typical operating conditions and is specifically defined so that the array efficiency ($\eta$) at 80 mW/m$^2$, NOCT, multiplied by the average annual incident irradiance on the array ($H$) equals the annual electrical energy produced by the array; i.e.,

$$E = \eta \times H$$

where

$E =$ total annual electrical energy (kW-hr/m$^2$)

$\eta =$ array efficiency at NOCT, 80 mW/m$^2$

$H =$ average annual irradiance on array at site of interest (kWh/m$^2$/year).

Studies* have shown that a working definition for NOCT that meets the above conditions is the module cell temperature corresponding to operation under the following specific Nominal Thermal Environment (NTE):

Irradiance = 80 mW/cm$^2$

Air temperature = 20°C

Wind average velocity = 1 m/sec

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The NOCT measurement shall be made by the procedure described in Appendix A.

B. ELECTRICAL DESIGN REQUIREMENTS

The electrical design of the module shall meet the following requirements:

1. Electrical Voltage Isolation

All module circuitry, including output terminations, shall be insulated from external surfaces. The voltage isolation design shall provide capability of withstanding a worst-case, open-circuit system voltage of 1000 Vdc, when modules are connected in series, at 100 mW/cm² irradiance and 0°C cell temperature. This capability shall be demonstrated by ability to withstand the 3000 Vdc high-voltage (hi-pot) test of Section III, Part B, Paragraph 4.

2. Electrical Grounding and Safety Construction Requirements

In order to minimize hazards, all modules shall meet the requirements contained in Interim Standard for Safety -- Flat-Plate Photovoltaic Modules, Volume I, Construction Requirements, JPL Document 5101-164, November 30, 1980. Because of the provisional nature of this document, requests for exceptions to specific requirements will be considered on a case-by-case basis at JPL's discretion.

3. Module Electrical Interface

The output terminations shall provide reliable connection to the module internal circuitry (cell strings) and shall have current-handling capability compatible with module short-circuit current. Corrosion-resistant materials and finishes shall be selected for terminations. Threaded stud terminations, if employed, shall be provided with a suitable enclosure or cover to prevent accidental exposure of any person to shock hazard. Threaded studs shall be of sufficient mechanical strength to withstand required fastener torque without loss of continuity to internal circuitry. Pigtails, if provided, shall be of sufficient length to provide connection to adjacent modules. The polarity of each termination shall be identified in accordance with the requirements of Section II, Part C, Paragraph 4, below.

4. Cell String Reliability and Redundancy

Circuit redundancy features shall be incorporated so that the loss in module output power at NOC shall be less than 10% under any of the following failure conditions:

(1) A single solar cell is separated into two parts by a single straight-line crack with any orientation or position within the cell.
(2) A single interconnect attachment point to a single solar cell is open-circuited.

(3) A single solar cell is short-circuited.

5. Module Hot-Spot Endurance

The module shall be capable of withstanding, for its design life, the hot-spot heating caused when the module is short-circuited at 100 mW/cm² solar irradiance, 40°C air temperature, and any of the following conditions occur:

(1) Shadowing of any portion of any single solar cell.

(2) Separation of any single solar cell into two parts by a single straight-line crack with any orientation or position within the cell.

(3) Open-circuiting of any single interconnect attachment point to any single solar cell.

(4) Short-circuiting of any single solar cell.

This capability shall be demonstrated by ability to withstand the Hot-Spot Endurance Test of Section III, Part B, Paragraph 5.

C. MECHANICAL DESIGN REQUIREMENTS

The mechanical design of the module shall meet the following requirements:

1. Module Geometry

The maximum module dimensions shall not exceed 1.22 x 2.44 m (4 x 8 ft). Mounting may be by means of through-fasteners or clamps. It is preferred that module dimensions and mounting provisions be designed to interface with the panel structure shown in Figure 2-1. However, when modules are provided with individual support frames, it is recommended that the dimensions and mounting pattern shown in Figure 2-2 be used. An Interface Control Drawing* shall be prepared by the manufacturer or contractor, providing at a minimum the following information:

(1) Maximum envelope dimensions and tolerances.

(2) Location, configuration, and materials and finishes of output terminations, with applicable user constraints (e.g., fastener torque and polarity identification).

*JPL Drawing No. 10082854 provides a suggested format for a suitable Interface Control Drawing.
Figure 2-1. Module-Panel Mounting Interface
Figure 2-2. Module Standard Dimensions
(3) Mounting hole or attachment provisions, materials and finishes of mounting surfaces or frames, dimensions, and tolerances.

(4) Illuminated (active) surface envelope dimensions and shadowing or view-angle constraints. The ratio of cell area to module maximum area shall be provided.

(5) Equipment grounding configuration, including location, materials and finishes, and user constraints (e.g., torque limits, surface sealing).

(6) A dimensioned cross-sectional view of the encapsulant systems through cells and interconnects.

(7) Dimensioned view(s) of both front and back contact cell-to-cell interconnect attachment geometry including strain-relief provisions.

(8) Electrical performance ($P_{avg}$ at NOC, $V_{no}$; $P_p$ at 100 mW/cm$^2$, 25°C)

(9) NOCT

(10) Maximum weight.

(11) Detail and location of manufacturer's identification label and, if applicable, caution labels.

(12) Installation and interconnection details, including typical system mechanical, electrical, and configuration constraints for a representative ILC array application.

2. Interchangeability

All modules from a given manufacturer shall be physically and functionally interchangeable. Any specialized equipment or tools required for module removal or replacement shall be fully described on the ICD. Tolerances on all external module dimensions shall be maintained to ensure module interchangeability. Surfaces, mounting holes, and any attachment hardware associated with the attachment interfaces shall be maintained within tolerances specified in the interface control drawing.

3. Optical Surface Soiling

The exposed optical surface(s) of the module shall be smooth and generally free of projections that could promote entrapment of dust and other debris. Particular attention shall be given to selection of materials for the optical surface(s) that will minimize the accumulation of non-removable contaminants, particulate matter and stains, and will promote self-cleaning by natural processes such as wind and rain.

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4. Module Labeling and Identification

Each module shall be identified in a permanent and legible manner with suitable labels or markings specifying the manufacturer's module model number (or drawing) and revision, sequential serial number, and maximum system operating voltage for which the module is designed. Additional information may include $V_{no}$ and $P_{avg}$. The identification shall be installed at a position that is accessible when the module is installed in the array. The polarity of each electrical termination shall be marked in a permanent and legible manner. Polarity marking shall be visible when acquiring access to the electrical terminations in a completed array.

D. ENVIRONMENTAL DESIGN REQUIREMENTS

Environments to be considered in assessing possible degradation of module electrical performance and physical properties include: solar exposure, particularly UV; thermal conditions, including freezing and thawing; effects of humidity, rain, snow, ice, hail, salt mist, and atmospheric oxidants; dust and debris accumulation; non-removable stains or contamination, and dynamic loading effects of wind, snow, hail, and mechanical handling. As a minimum, the module design shall be capable of withstanding exposure to the following environmental test environments:

1. Thermal cycle test from $-40^\circ C$ to $+90^\circ C$ in accordance with the Test Procedure in Section V, Part A, of this specification.

2. Humidity-freezing cycle test in accordance with the Test Procedure in Section V, Part B.

3. Mechanical cyclic loading in accordance with the Test Procedure in Section V, Part C. The test load level shall be 2.4 kPa (50 lb/ft$^2$).

4. Twisted mounting surface of 20 mm/m (.25 in./ft) in accordance with the Test Procedure in Section V, Part D.

5. Hail impact testing in accordance with the Test Procedure in Section V, Part E. The module shall be capable of withstanding 25.4 mm (1.0 in.)-dia simulated hailstone impact.

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SECTION III
CHARACTERIZATION, QUALIFICATION AND ACCEPTANCE TEST REQUIREMENTS

A. PERFORMANCE CHARACTERIZATION TEST REQUIREMENTS

The tests included in this section shall be performed to characterize module electrical and thermal performance. The characterization testing shall be performed in the sequence shown in the flow diagram in Figure 3-1.

1. Determination of Nominal Operating Cell Temperature

For purposes of providing a measurement of module performance that is representative of the expected terrestrial application, module performance measurements are referenced to the NOCT. NOCT is determined by measuring the average cell temperature in the module under ambient conditions equivalent to the NTE. The NTE is characterized by 80 mW/cm² insolation, ambient air temperature of 20°C and average wind velocity of 1.0 m/sec, with the module mounted to simulate the application thermal boundary conditions, and electrical output terminations open-circuited. Actual cell temperatures shall be taken at conditions approximating NTE in order to obtain the solar cell NOCT. The approved techniques for performing the NOCT characterization test are included in Appendix A.

2. Electrical Measurements

Module power values are required at two sets of operating conditions as follows: (1) at NOC and $V_{no}$ for Module Output Power ($P_o$) and (2) at 100 mW/cm², AM 1.5, 25°C and maximum power for Module Peak Power Rating ($P_{p}$). Since it is unlikely that the operating conditions specified in the definition of the power values will exist during testing, these power values may be computed using procedures such as that described in Section IV, Part A, Paragraph 2. Therefore, in addition to determining NOCT as described above, it is necessary to determine temperature correction coefficients by a procedure such as that described in Appendix B. Note that that procedure is valid for a specific value of irradiance.

B. DESIGN QUALIFICATION TEST REQUIREMENTS

This section specifies the minimum tests that shall be performed by the contractor or manufacturer in order to ensure that the modules will satisfy the design requirements of this specification and to provide confidence that production modules will function within the specified performance requirements.

Modules shall be mounted on structural test frames simulating the selected mounting interface and configuration for all design qualification testing.

Modules shall be provided with suitable circuit monitoring instrumentation. To the extent possible, the normal module output termination connections shall be used for this instrumentation. Temperature instrumentation (e.g. thermocouples) shall be installed on the module's rear.
surface, positioned directly behind a centrally located solar cell. At a minimum, the following qualification tests shall be performed in the order described below. For clarification, the test sequence is shown in the flow diagram (Figure 3-1).

1. Baseline Electrical Measurement

After assembly in the structural test frame, each module shall be measured to establish a baseline electrical output power that will serve as the comparison value for determination of the effects of qualification testing on electrical performance. The measurement shall be made in accordance with Section IV, Part A.

2. Baseline Visual Inspection

Each module shall be visually inspected to obtain a baseline determination of the presence or absence of defects in the module for purposes of detecting any changes after environmental exposure. Pertinent sections of the applicable acceptance or rejection criteria or workmanship specification shall provide a guide for this inspection.

3. Ground Continuity Test

Each module having exposed external conductive surfaces (i.e., frame, structural members or edge closures) shall be tested using a suitable continuity tester to verify that electrical continuity exists between all such surfaces and the module grounding point. Modules that use direct attachment to the array structure to obtain grounding shall be tested after assembly.

4. Electrical Isolation Test

Each module shall be subjected to both positive- and negative-polarity dc hi-pot testing conducted with the output terminations short-circuited. Test leads from a suitable dc-voltage power supply shall be connected to the shorted terminals and the module grounding point. In the case of modules not required to provide an equipment ground stud, the mounting structure shall be used as the alternative test point. Voltage shall be applied at a rate not to exceed 500 V/sec up to the test voltage of 3000 Vdc, and then held at the required test voltage for 1 min. The module shall be observed during the test and there shall be no signs of arcing or flash-over. Dc leakage current shall be monitored during the test and shall not exceed 50 μA. Ac leakage current due to capacitive charging and power supply ripple shall not be considered. Dc leakage current during the test shall be recorded for each polarity.

5. Hot-Spot Endurance Test

One module shall be subjected to a hot-spot endurance test for a cumulative exposure period of 100 h. The test shall be conducted using
Figure 3.1. Characterization and Qualification Test Flow Plan
the procedures described in Appendix C. The sample shall satisfy the criteria for qualification stated in Section III, Part B, Paragraph 7, at conclusion of the test. The sample subjected to the hot-spot endurance test shall not be subjected to the environmental test sequence (Section III, Part B, Paragraph 6).

6. Environmental Tests

Modules selected for environmental tests shall be subjected to the following exposures which are described in detail in Section V. Module electrical performance measurements and visual inspection shall be conducted after each exposure. The tests shall be conducted in the order indicated:

(1) Thermal cycle test.
(2) Humidity-freezing cycle test.
(3) Mechanical loading test.
(4) Twisted-mounting surface test.
(5) Hail impact test.

7. Qualification Pass-or-Fail Criteria

The output power degradation of each tested module, determined after completion of all required qualification tests, shall not exceed 5% of the baseline electrical performance determined in accordance with Section III, Part B, Paragraph 1. The module shall not have exhibited any open-circuits or ground faults during performance of the environmental tests. The module shall pass the electrical isolation test (Section III, Part B, Paragraph 4) when retested at completion of qualification tests. The allowable level of observable cracks or other mechanical degradation (such as delamination of coatings) shall be that established by the JPL-approved visual inspection criteria for production modules (Section III, Part C, Paragraph 3).

C. MODULE PRODUCTION ACCEPTANCE REQUIREMENTS

Module acceptance shall be based on meeting the following requirements:

1. Electrical Performance

Each module shall be measured to obtain its current-voltage characteristics (I-V curve). The module output power at NOC and V_{no} shall be determined in accordance with Section IV, Part A. No module shall be accepted for delivery that produces less than 90% of the P_{avg}, established under Nominal Operating Conditions.
2. Electrical Isolation

Each module shall withstand the dc hi-pot test, in accordance with Section III, Part B, Paragraph 4, to assure adequate electrical isolation for personal safety at system operating voltages.

3. Mechanical and Visual Inspection

Modules shall be mechanically and visually inspected on the basis of criteria developed by the manufacturer and approved by JPL, defining acceptable and rejectable standards of workmanship and quality. Acceptance/Rejection Criteria for JPL/LSA Modules, JPL Document 5101-21, Revision B, November 3, 1978, provides guidelines for development of suitable criteria.
SECTION IV

PERFORMANCE MEASUREMENT PROCEDURES

A. ELECTRICAL PERFORMANCE

Electrical performance measurements shall be referenced to NOC, defined
as 80 mW/cm² irradiance, AM1.5, NOCT. All procedures, equipment and standards
related to measurements shall conform to the current revision of Terrestrial
Photovoltaic Measurement Procedures, NASA TM 73702. A reference cell which has
spectral response representative of the cells in the module shall be the only
irradiance reference used. Secondary standards or transfer modules shall not
be used.

To provide for efficient module testing, module performance measurements
may be conducted at either NOC conditions or at Optional Test Conditions
(OTC), defined as 80 mW/cm² irradiance and a cell temperature other than
NOCT, and then corrected to the required reporting conditions.

1. Module Output Power Measurements at NOC

When module performance is measured at NOC, the individual module
output power shall be calculated as the product of $V_{no}$ and the module
current taken from the I-V characteristic curve at $V_{no}$:

$$ P = V_{no} \cdot I_{NOCT} $$

where

- $P$ = individual module output power
- $V_{no}$ = module nominal operating voltage at NOCT
- $I_{NOCT}$ = module current at NOCT and $V_{no}$

2. Module Output Power Measurements at OTC

When module performance is measured at OTC, the individual module
output power must be determined by application of appropriate temperature
correction coefficients to the voltage and current data obtained from the OTC
I-V characteristic curve. Under these conditions the module output power is
calculated directly from:

$$ P = V_{no} (I_{OTC} + \Delta I) $$

where

- $I_{OTC}$ = current at $V_{OTC}$ from OTC I-V curve
- $\Delta I$ = current temperature correction, amperes
\[ \Delta I = C_I (T_{NOCT} - T_{OTC}) \]

\[ C_I = \text{current temperature coefficient, A/}^{\circ}\text{C} \]

\[ V_{OTC} = \text{voltage point on OTC I-V curve} \]

\[ V_{OTC} = V_{no} - \Delta V \]

\[ \Delta V = \text{voltage temperature correction, volts} \]

\[ \Delta V = C_V (T_{NOCT} - T_{OTC}) \]

\[ C_V = \text{voltage temperature coefficient (usually negative), V/}^{\circ}\text{C} \]

\[ T_{NOCT} = \text{predetermined value of NOCT} \]

\[ T_{OTC} = \text{selected optional test temperature} \]

Determination of the temperature coefficients shall be accomplished by the method prescribed in Appendix B.

Alternative temperature correction procedures such as that provided by computer-controlled Large-Area Pulsed Solar Simulator (xenon source) may be used in approved by JPL.
SECTION V
ENVIRONMENTAL TEST PROCEDURES

A. THERMAL CYCLE TEST PROCEDURE

Sample modules shall be subjected to the thermal cycle test in accordance with the profile shown in Figure 5-1, with the cell temperature varying between -40°C and +90°C. The temperature shall vary approximately linearly with time at a rate not exceeding 100°C/h and with a period not greater than 6 h per cycle (from ambient to -40°C to +90°C to ambient). The module circuitry shall be instrumented and monitored throughout the test to verify that no open circuits or ground faults occur during the exposure. All environmental test modules shall receive 50 cycles of exposure. Two samples shall be returned to test after the initial 50 cycles for a total accumulation of 200 cycles while 50-cycle-only samples (two modules, minimum) are subjected to other environmental tests.

B. HUMIDITY-FREEZING CYCLE TEST PROCEDURE

The module shall be subjected to a humidity-freezing cycle test in accordance with the profile shown in Figure 5-2. The module circuitry shall be instrumented to verify that no open circuits or ground faults occur during the exposure. The electrical performance test, in accordance with Section IV, Part A, shall be performed within 1 h after removal from the humidity chamber, or within another mutually agreed-upon time period if the testing is subcontracted.

C. MECHANICAL LOADING TEST PROCEDURE

Panel-type modules shall be subjected to a cyclic load test in which the module is supported only at the design support points and a uniform load normal to the module surface is cycled 10,000 times in alternating negative and positive directions. Cycle rate shall not exceed 20 cycles/min. The module circuitry shall be instrumented to verify that no open circuits or ground faults occur during the test. Cyclic Pressure-Load Developmental Testing of Solar Panels, JPL Document 5101-19, February 1977, describes techniques suitable for the performance of this test.

D. TWISTED-MOUNTING SURFACE TEST PROCEDURE

The module shall be subjected to a twist test by deflection of the structure or surface to which it is mounted. The deviation from a true flat surface during the test shall be 20 mm/m (0.25 in. per foot), measured along either mounting surface in the direction of the module's width, as shown in Figure 5-3. The module circuits shall be instrumented to verify that no open circuits or ground faults occur during the deflection test.
Figure 5-1. Thermal Cycle Test (Shorter cycle time is acceptable if 100°C/hr maximum rate of temperature change is not exceeded. Chamber may be opened at 25-cycle intervals for visual inspection.)

Figure 5-2. Humidity-Freezing Cycle Test
Figure 5-3. Graphic Representation of Twisted-Mounting Surface Requirement. Points A, B, C, D are in a Plane; Point A' is out of Plane by the Amount Shown

E. HAIL-IMPACT TEST PROCEDURE

The module shall be subjected to normal impact loading with 25.4-mm (1 in.)-dia ice balls traveling at terminal velocity of 23.2 m/sec (52 mph).

At least 10 of the specimen's most sensitive points shall be selected for impact. The most sensitive exposed points on a test specimen may be determined experimentally through destructive testing of a sample panel. Ice balls of 25 mm (1.0 in.) dia shall be fired at candidate sensitive points with increasing velocity until the panel is broken. Several different points on the panel should be broken, and the points broken at the lowest velocities should be used for subsequent testing.
The candidate points selected should include (where applicable) the following:

(1) Center points of cells.

(2) Corners and edges of the module.

(3) Edges of cells, especially around electrical contacts.

(4) Points of minimum spacing between cells.

(5) Points of support for any superstrate material.

(6) Points of maximum distance from points of support in (5).

Error of up to 13 mm (0.5 in.) in the location of a hit is acceptable. Either pneumatic or spring-actuated guns for projecting the ice-balls against the modules are acceptable. However, ice-ball velocity at impact must be controlled to within +5% of specified velocity for the required hailstone size. Ice balls shall be generally spherical in shape with a maximum deviation in diameter of 3 mm (0.125 in.). The iceballs shall be cooled to 

\[-10^\circ C \pm 2^\circ C\] as measured in the compartment where they are stored. The module shall be mounted in manner representative of that used for actual installation of the module in the array. After each impact, the module shall be inspected for visible evidence of damage. Note that ice balls are the only acceptable hailstone simulation. Steel balls, for example, shall not be used. Resistance of Photovoltaic Solar Panels to Simulated Hail, JPL Document 5101-62, April 1978, describes techniques and equipment suitable for performance of this test.
APPENDIX A

DETERMINATION OF NOMINAL OPERATING CELL TEMPERATURE

1. Purpose

The purpose of this test is to acquire sufficient data to allow an accurate determination of the nominal operating temperatures of the solar cells of a terrestrial solar array module.

A working definition for NOCT is the module cell temperature corresponding to operation under the following specific Nominal Thermal Environment (NTE):

Irradiance = 80 mW/cm²
Air temperature = 20°C
Wind average velocity = 1 m/sec
Mounting = oriented normal to solar noon, mounting per typical array installation
Electrical load = open circuit

The NOCT test procedure is based on gathering actual measured cell temperature data by means of thermocouples attached directly to the cells of interest, for a range of environmental conditions similar to the NTE. The data are then presented in a way that allows accurate and repeatable interpolation of the NOCT temperature.

2. Determination of NOCT

The temperature of the solar cell (T_{cell}) is primarily a function of the air temperature (T_{air}), the average wind velocity (V), and the total solar irradiance (I) impinging on the active side of the solar array module. The approach for determining NOCT is based on the fact that the temperature difference (T_{cell} - T_{air}) is largely independent of air temperature and is essentially linearly proportional to the irradiance level. Analyses indicate that the linear assumption is quite good for irradiance levels greater than about 40 mW/cm². The procedure calls for plotting (T_{cell} - T_{air}) against the irradiance level for a period when wind conditions are favorable. The NOCT value is then determined by adding T_{air} = 20°C to the value of (T_{cell} - T_{air}) interpolated for the NTE irradiance level of 80 mW/cm², i.e., NOCT = (T_{cell} - T_{air}) at NTE + 20°C.

The plot of (T_{cell} - T_{air}) vs L shall be determined by conducting a minimum of two field tests in which the module being characterized is tested under terrestrial environmental conditions approximating the NTE in accordance with the testing guidelines that follow. Each test shall include acquiring a semi-continuous record of (T_{cell} - T_{air}) over a one or two-day period, together with other measurements as required to characterize the terrestrial
measurements made when the average wind velocity is 1 m/sec +0.75 m/sec and with gusts less than 4 m/sec for a period of 10 minutes before and up to the time of measurement. Local air temperature during the test period shall not vary by more than 5°C and shall lie in the range of 20°C ±15°C. Using only acceptable data as defined, a plot shall be constructed from a set of measurements made either before or after solar noon that defines the relationship between \( (T_{cell} - T_{air}) \) and the irradiance level \( (L) \) for \( L \geq 40 \text{ mW/cm}^2 \).

When \( (T_{cell} - T_{air}) \) is plotted as a function of \( L \) for average wind velocities less than 1.75 m/sec, results similar to those shown in Figure A-1 are obtained. For the data shown, the local air temperature was 15.6°C ±4.5°C and the wind speed varied from 0 to less than 4 m/sec with an average of 1 m/sec. Using the plot of \( (T_{cell} - T_{air}) \) vs \( L \), the value of \( (T_{cell} - T_{air}) \) at NTE is determined by interpolating the average value of \( (T_{cell} - T_{air}) \) for \( L = 80 \text{ mW/cm}^2 \). Using the data in Figure A-1 as an example, \( (T_{cell} - T_{air}) \) at NTE is determined to be 20.2°C. The preliminary value of NOCT is thus 20.2°C +20°C = 40.2°C.

3. Air Temperature and Wind Correction

A correction factor to the preliminary NOCT for average air temperature and wind velocity is determined from Figure A-2. This value is added to the preliminary NOCT and corrects the data to 20°C and 1 m/sec. \( T_{air} \) and \( V \) are the average temperature and wind velocity for the test period.

For the test data shown in Figure A-1, \( \bar{V} \) is 1 m/sec and \( T_{air} \) is 15.6°C. From Figure A-2, the correction factor is 0°C. The NOCT is therefore 40.2°C.

4. Test Geometry

a. Tilt Angle. The plane of the module shall be positioned so that it is normal to the sun (±5°) at solar noon.

b. Height. The bottom edge of the module shall be 0.6 m (2 ft) or more above the local horizontal plane or ground level.

c. Panel Configuration. The module shall be situated in the interior of a planar surface that extends 2 ft beyond the module in all directions, and that is designed to simulate the thermal boundary conditions of the expected field installation. For modules designed for free-standing, open-back installations, black aluminum plates or other modules of the same

*The effect of air temperature variation of more than 5°C during the test period will appear as an increase in the scatter of plotted data, resulting in poor correlation between the data and the resultant curve.
design shall be used to fill out the remaining open area of the planar surface. During testing the module should be supported in a manner that allows normal cooling of the rear surface. In the case of modules that are not self-supporting or have special mounting characteristics, the test module shall be centrally located in the planar surface and integrated with representative supporting structure and interfacing modules to simulate the thermal boundary conditions expected in field application.

d. Surrounding Area. There shall be no obstructions to prevent full irradiance of the module beginning a minimum of 4 h before solar noon and up to 4 h after solar noon. The ground surrounding the module shall not have an abnormally high solar reflectance and shall be flat and level, or sloping away from the test fixture in all directions. Grass, various types of ground covers, blacktop, or dirt are acceptable for the local surrounding area. Buildings or other objects having a large solar-reflective finish shall not be in the immediate vicinity. Good engineering judgment shall be exercised to ensure that both front and back sides of the module receive a minimum of reflected solar energy from the surrounding area.

e. Wind Direction. The wind shall not be predominantly from due east or due west; flow parallel to the plane of the array can result in a lower-than-typical NOCT and is not acceptable. Artificial wind generation equipment such as fans shall not be used.

f. Module Electrical Load. In order to simplify testing, data shall be obtained for a module open-circuit condition corresponding to zero electrical power output.

Figure A-1. Typical Cell Temperature Data
Figure A-2. NOCT Correction Factor
5. Test Equipment

a. Pyranometer. The total solar irradiance on the active side of the module shall be measured by a pyranometer mounted on the plane of the module and within 0.3 m (1 ft) of the array. The pyranometer used shall have a traceable annual calibration to a recognized standard instrument and shall be either an Epply PSP or an equivalent Class 1 pyranometer as defined by the World Meteorological Organization.

b. Wind Measurement. Both the wind direction and wind speed shall be measured at the approximate height of the module and as near to one of the sides of the module as feasible.

c. Air Temperature. Local air temperature shall be measured at the approximate height of the module. The measurement shall be made in the shadow of the module and shall be accurate to ±1°C. An average local air temperature is desired. This is obtained satisfactorily by increasing the thermal mass of the thermocouple by imbedding the thermocouple in a solder sphere of approximately 6 mm (0.25 in.) dia. The thermocouple must be appropriately shielded and vented.

d. Cell Temperature. The temperature of at least two representative interior solar cells shall be measured to ±1°C. Thermocouples shall be 36 gauge, and shall be soft-soldered directly to the back of the cells.

e. Substrate Surface Temperature. The exterior temperature of the rear of the solar module shall be measured to ±1°C beneath a representative cell and when practical beneath a representative space between cells. Thermocouples shall be 26 gauge, and shall be bonded down with aluminized epoxy adhesive.

6. Data Recording

All data shall be printed out approximately every 2 min. In addition, solar intensity, wind speed, wind direction, and air temperature shall be continuously recorded.

7. Cleaning

The active side of the solar cell module and the pyranometer bulb shall be cleaned before the start of each test. Dirt shall not be allowed to build up. Cleaning with a mild soap solution followed by a rinse with distilled water has proven to be effective.

8. Equipment Calibration

A calibration check shall be made of all the equipment before the start of the test and re-verified following the test.
APPENDIX B

DETERMINATION OF TEMPERATURE CORRECTION COEFFICIENTS

1. Purpose

The purpose of this test is to determine the temperature correction coefficients used in transforming module electrical performance measurements made at OTC to NOC.

2. Approach

A photovoltaic I-V characteristic curve obtained at a given cell temperature and a fixed irradiance level can be transformed by a point-by-point correction to an I-V curve at a different temperature. For purposes of translation from OTC to NOC, the irradiance is constant at 80 mW/cm². The region of the I-V curve of interest is near the maximum power point. The current and voltage points on the NOC I-V curve can be obtained from the coordinates of points on the OTC I-V curve with the following equations:

\[
I_{\text{NOC}} = I_{\text{OTC}} + C_I (T_N - T_O)
\]

(1)

\[
V_{\text{NOC}} = V_{\text{OTC}} + C_V (T_N - T_O)
\]

(2)

where

- \(V_{\text{OTC}}, I_{\text{OTC}}\) are coordinates of a selected point on the curve obtained at OTC.
- \(V_{\text{NOC}}, I_{\text{NOC}}\) are coordinates of the corresponding point on the NOC curve.
- \(T_N\) is actual cell temperature, usually NOCT \(\pm 2^\circ\text{C}\), during the NOC curve measurement.
- \(T_O\) is actual cell temperature during the OTC curve measurement.
- \(C_I\) is the current-temperature coefficient, expressed as A/°C.
- \(C_V\) is the voltage-temperature coefficient, expressed as V/°C. (usually a negative value).

The values of \(C_I\) and \(C_V\) are to be determined empirically by a curve overlay procedure applied to I-V curve measurements of a minimum of 10 modules (or alternative quantity approved by JPL) with cell temperatures approximating both OTC and NOCT. The values of \(C_I\) and \(C_V\) for the 10 modules will be averaged to establish mean values to be used in calculating the power of production modules.
3. Procedure

To determine $C_I$ and $C_V$, the following procedure shall be used:

1. Install the module to be tested in a temperature-controlled environment. After stabilizing the module temperature at the cell temperature selected for OTC within $\pm 2^\circ C$, obtain an I-V curve for OTC conditions. Record the actual temperature ($T_0$).

2. Repeat Step (1) for NOC with the module stabilized at NOCT $\pm 2^\circ C$. Record the actual temperature ($T_N$).

3. On the curve obtained at NOC, mark two points, near the maximum power point, and approximately equidistant from it. For reference these points should be approximately at 90% of $I_{SC}$ and 60% of $V_{OC}$.

4. Using a light box or similar equipment, superimpose the OTC curve on the NOC curve and translate the curve rectilinearly until the curves match closely at the marked points. Mark the overlaid curve at the same points.

5. Separate the curves and determine the voltage ($V_{NOC} - V_{OTC}$) and current ($I_{NOC} - I_{OTC}$) shifts required to achieve the match.

6. Calculate the $C_I$ and $C_V$ from the following:

$$C_I = \frac{I_{NOC} - I_{OTC}}{T_N - T_O}$$

$$C_V = \frac{V_{NOC} - V_{OTC}}{T_N - T_O}; \text{ } C_V \text{ is usually negative.}$$

7. Determine the average values of $C_I$ and $C_V$ for the sample modules.
APPENDIX C
HOT-SPOT ENDURANCE TEST PROCEDURE

1. Purpose

The purpose of this test procedure is to evaluate the ability of a module to endure the long-term effects of periodic hot-spot heating associated with common fault conditions such as severely cracked or mismatched cells, single-point open-circuit failures, or non-uniform illumination (partial shadowing).

2. Commentary

Field experience indicates that periodic circuit faults such as partial shadowing, cracking of cells, and interconnect open circuits must be expected to occur even in highly reliable arrays. Under these fault conditions it is desirable to ensure that possible hot-spot heating due to reverse biasing does not cause propagation of the fault or electrical safety hazards through such mechanisms as solder melting or encapsulant deterioration.

Hot-spot heating is caused when module operating current levels exceed the reduced short-circuit current level of an individual cell or group of cells in an array circuit. The reduced short-circuit current fault condition can be the result of a variety of causes including non-uniform illumination (local shadowing), individual cell degradation due to cracking or soiling, or loss of a portion of a series-parallel circuit due to individual interconnect open circuits. Under this condition the cell(s) carrying the excess current dissipate power equal to the product of the current and the reversed voltage that develops across the cell(s), which can heat the cell(s) to elevated temperatures.

3. Test Procedure

The preferred procedure for conducting this test includes a series of steps, first to select and instrument appropriate cells for testing, then to determine the hot-spot test levels, and last to conduct the hot-spot endurance test.

a. Cell Selection and Instrumentation. The degree of hot-spot heating within an affected cell is dependent on a variety of conditions, including the module series-parallelizing, the amount of overall illumination, the amount of over-current in the affected cell, and the reverse-voltage I-V characteristics of the affected cell(s). Because the reverse-voltage I-V characteristics vary considerably from cell to cell within a given module, it is necessary first to determine the dark reverse-voltage I-V curve for a representative sample of cells (at least 10) within the test module. This can be done by directly accessing individual cells or by obtaining data for
multiple points using the shadow technique.* The cell's dark characteristics should be determined for reverse voltages from 0 to \( V_L \) or currents from 0 to \( I_L \), whichever limit is reached first, where:

\[
I_L = I_{SC} \text{ of an average cell at 100 mW/cm}^2, \text{ NOCT.}
\]

\[
V_L = N \times V_{mp} \text{ of an average cell at 100 mW/cm}^2, \text{ NOCT.}
\]

\[
N = \text{Number of series cells per bypass diode or number of series cells per module, whichever is less.}
\]

When the family of reverse-voltage I-V curves is plotted for the representative sample of cells, a graph similar to Figure C-1 should be obtained. The individual curves may be either all voltage-limited (Type A), current-limited (Type B), or a combination of both (as shown). In general, the cells associated with the highest hot-spot heating levels are those with the highest shunt resistance, although low shunt resistance may be associated with highly localized heating.

For testing, select three non-adjacent individual cells within the test module from the measured sample: one representative of the highest shunt resistance obtained, one representative of the average, and one representative of the lowest. Provide the test cells with positive and negative electrical leads to allow them to be connected individually to separate power supplies. Eliminate parallel current paths around the selected test cells by disrupting cell-to-cell connections as necessary. The lead attachment should minimize disruption of the cell's heat-transfer characteristics or the hot-spot endurance of the encapsulant system.

b. Selection of Hot-Spot Test Level. The objective of this portion of the test procedure is to select the level of heating and the corresponding test condition that will stress the module in a manner similar to a severe hot-spot field condition. The severity of the field condition will depend on the array circuit configuration, the array I-V operating point, the ambient thermal conditions, the overall irradiance level, and the previously described characteristics of the affected cells. In particular, whether the cells are Type A or Type B is important.

For Type A Cells. The maximum cell reverse voltage \( (V_L) \), the cell illumination level, and the ambient thermal environment are the key parameters. When a module is incorporated into an array branch circuit the maximum reverse voltage imposed on an individual cell can approach the maximum array operating voltage. In this procedure it is assumed that the array

designer has used bypass diodes or other means to limit the reverse voltage on any single module to less than one volt. The test voltage \(V_{\text{test}}\) is thus set to yield the maximum reverse voltage that can be applied across a single cell when the test module is shorted with a bypass diode. For type A cells, \(V_{\text{test}}\) shall be set equal to \(N\) times the average \(V_{\text{mp}}\) of an individual cell, where \(N\) is the number of series cells per bypass diode, or the number of series cells per module, whichever is less.

The second key test condition for Type A cells is the illumination level; it directly controls the hot-spot test current \(I_{\text{test}}\) level, and therefore the power level. As shown in Figure C-2, there is a unique illumination level that corresponds to worst-case power dissipation for any particular Type A solar cell. In the test, the irradiance level on the test cell shall be adjusted to achieve this worst-case condition with \(I_{\text{test}}\) set equal to the average cell maximum power current at 100 mW/cm², NOCT.

The third test condition is the level of the ambient thermal environment. For test purposes a nominal terrestrial environment shall be created by using an air temperature of \(20^\circ +5^\circ\text{C}\) and a radiant heating source to achieve a uniform background module cell temperature equal to NOCT.

**Type B Cells.** Type B cells have a cell shunt resistance so low that the maximum reverse test voltage \(V_{\text{test}}\) is set by the I-R drop across the cell associated with the available current level. In these cells, worst-case heating occurs when the test cell is totally shadowed, and the current level is at a maximum. Test conditions for Type B cells shall therefore maintain a cell illumination level of less than 5 mW/cm² (to allow
for room lighting and an IR heating source) and a current test level ($I_{test}$) equal to the short-circuit current of an average cell at 100 mW/cm², NOCT.

As with type A cells, a uniform background cell temperature equal to NOCT ±5°C shall be created by using an air temperature of 20°C ±5°C, together with a radiant heating source.

c. Test Execution. Detailed steps for execution of the test involves subjecting the three selected test cells to cyclic hot-spot heating, at the levels determined above, for a period of 100 h total on-time, are as follows:

(1) Connect a separate dual limiting constant-current constant-voltage power supply to each test cell with polarity arranged to drive the cells with reverse voltage. Adjust the voltage and current limits to the $V_{test}$ and $I_{test}$ values determined in Step b above.
(2) Apply an IR radiant-heating source with a visible light contribution below 5 mW/cm² to the module front surface and adjust the heating level to achieve a uniform module cell temperature equal to NOCT +2°C. The ambient air should be still and at a temperature of 20° +5°C.

(3) For Type A cells only, arrange an additional light source to illuminate each test cell to the unique level determined in Step b (Figure C-2). This is most easily accomplished after the power supply and IR source are turned on by adjusting the illumination level to achieve, simultaneously, both current and voltage limiting at I_test and V_test after equilibrium test conditions stabilize. The illumination source should not contain excessive infrared or ultraviolet irradiance that would lead to abnormal heating or accelerated UV aging. ELH type tungsten sources have been found to meet this requirement.

(4) Connect the power supplies, IR source, and light sources to an appropriate timer to obtain a cyclic on-off operation with an on-time equal to 1 h, and an off-time sufficient to allow the test cells to cool to within 10°C of the ambient air temperature.

(5) Conduct the test until a total of 100 h of on-time has been accumulated.

(6) Visually inspect the test cells and adjacent areas of the encapsulation system at approximately 24-h intervals during the test while under bias conditions, and upon completion of the 100-h sequence. Identify any evidence of degradation, including delamination, outgassing or blistering of encapsulants, cell cracking, solder melting or other defects as defined by the JPL-approved visual inspection criteria (Section III, Part B, Paragraph 6) resulting from this test. Measure the post-test electrical performance of the module for comparison with baseline electrical performance per Section IV, Part A. Perform a post-test electrical isolation test in accordance with Section III, Part B, Paragraph 4.